

SY DIPLOMA - DTE (IT)

Sr.No.	Topic	No. of times appeared in last 5 question papers
1	Boolean eq. using basic gates & simplification	10
2	K-map	10
3	SOP & POS example & minterm & maxterm, sop to pos & pos to sop conversion	7
4	Number system conversion	6
5	T flip flop	5
6	SR flip-flop	4
7	JK flip-flop	4
8	universal gate, derive basic gates using NAND / NOR	3
9	2'compliment subtraction	3
10	logic gates with their types	3
11	D flip flop	3
12	Half adder	3
13	Compare sequential logic circuit and combinational logic circuit	3
14	Full adder	2
15	De-Morgan's law	2
16	compare TTL and CMOS logic families	2
17	Necessity of MUX	2
18	Preset & clear	1
19	16:1 MUX using 4:1 MUX / 8:1 MUX	1
20	1' complement subtraction	1
21	BCD addition & binary addition	1
22	Different triggering methods	1
23	1:4 DE-MUX using logic gates	1
24	Characteristics of digital IC	1